

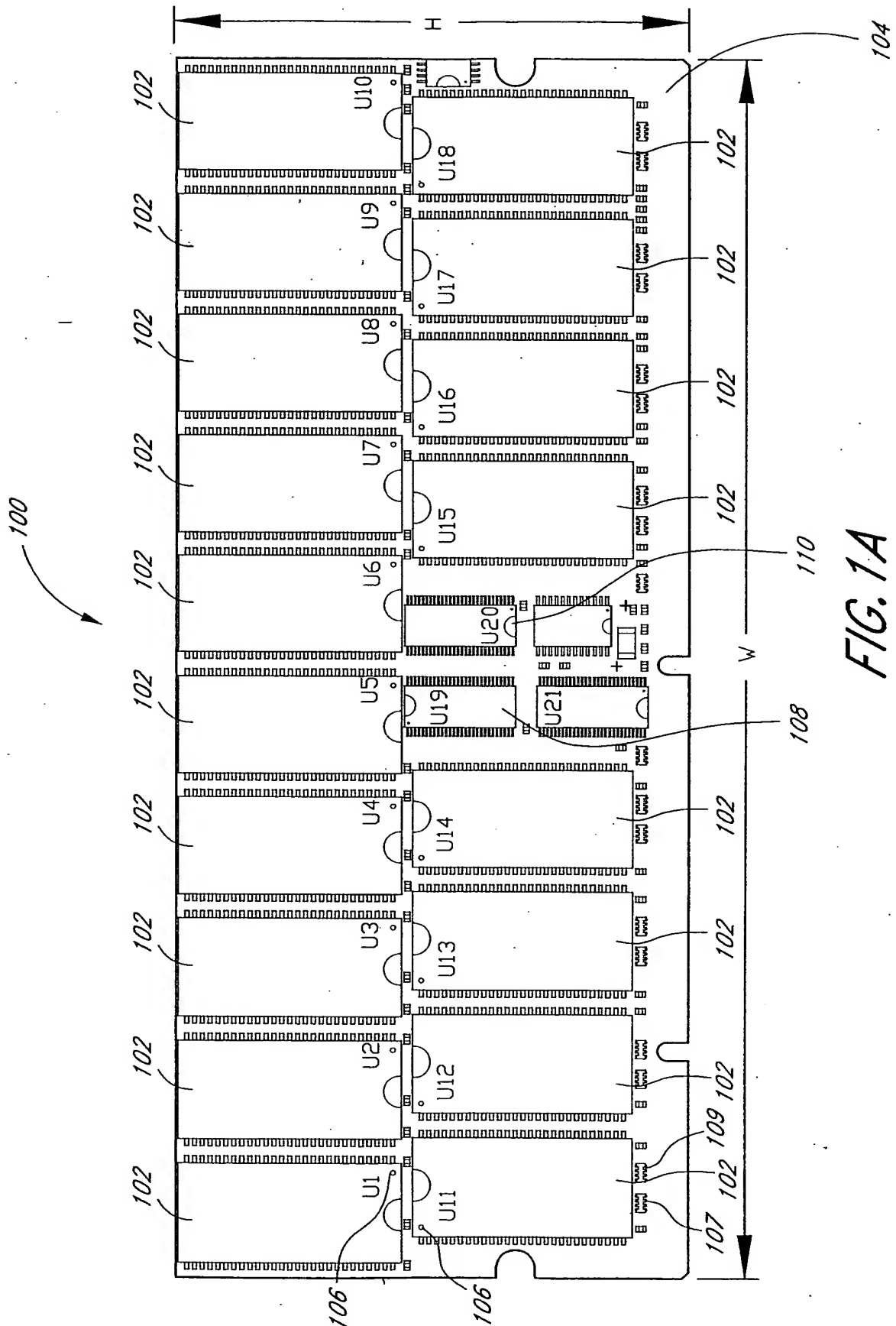
ARRANGEMENT OF INTEGRATED CIRCUITS IN A MEMORY
MODULE

Bhakta, et al.

Appl. No.: Unknown

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MODULE

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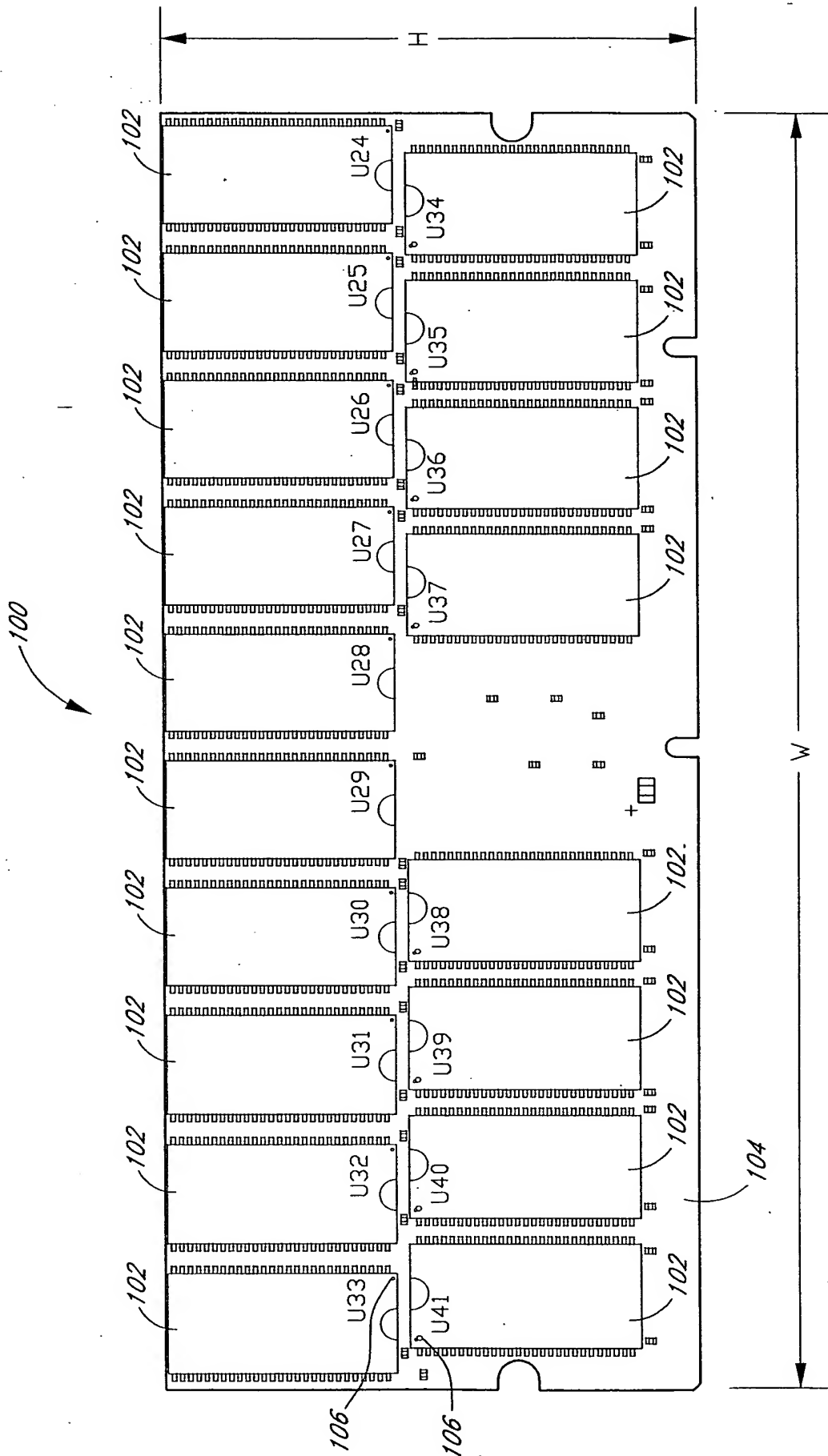


FIG. 1B

ARRANGEMENT OF INTEGRATED CIRCUITS IN A MEMORY
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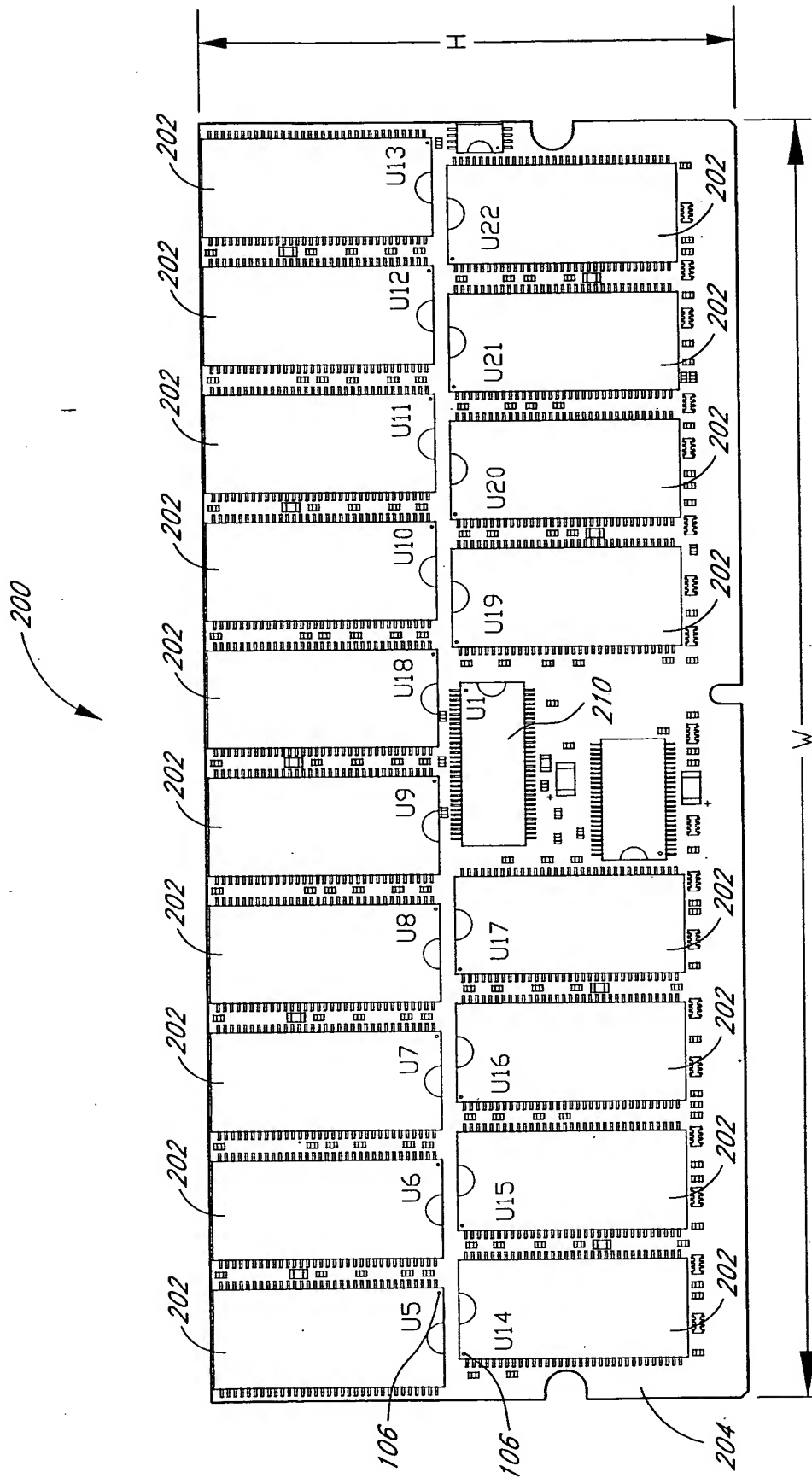


FIG. 2A

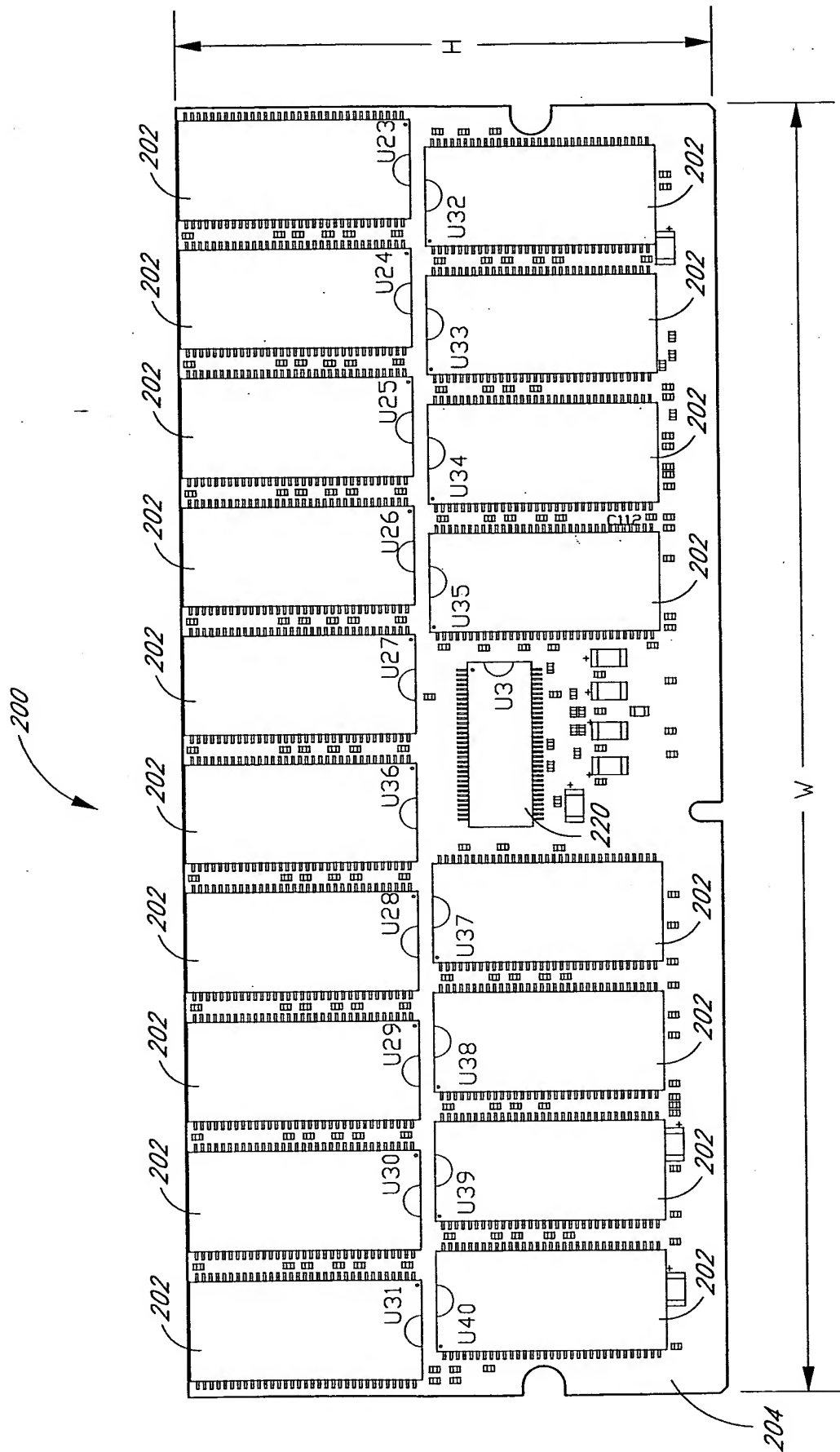


FIG. 2B

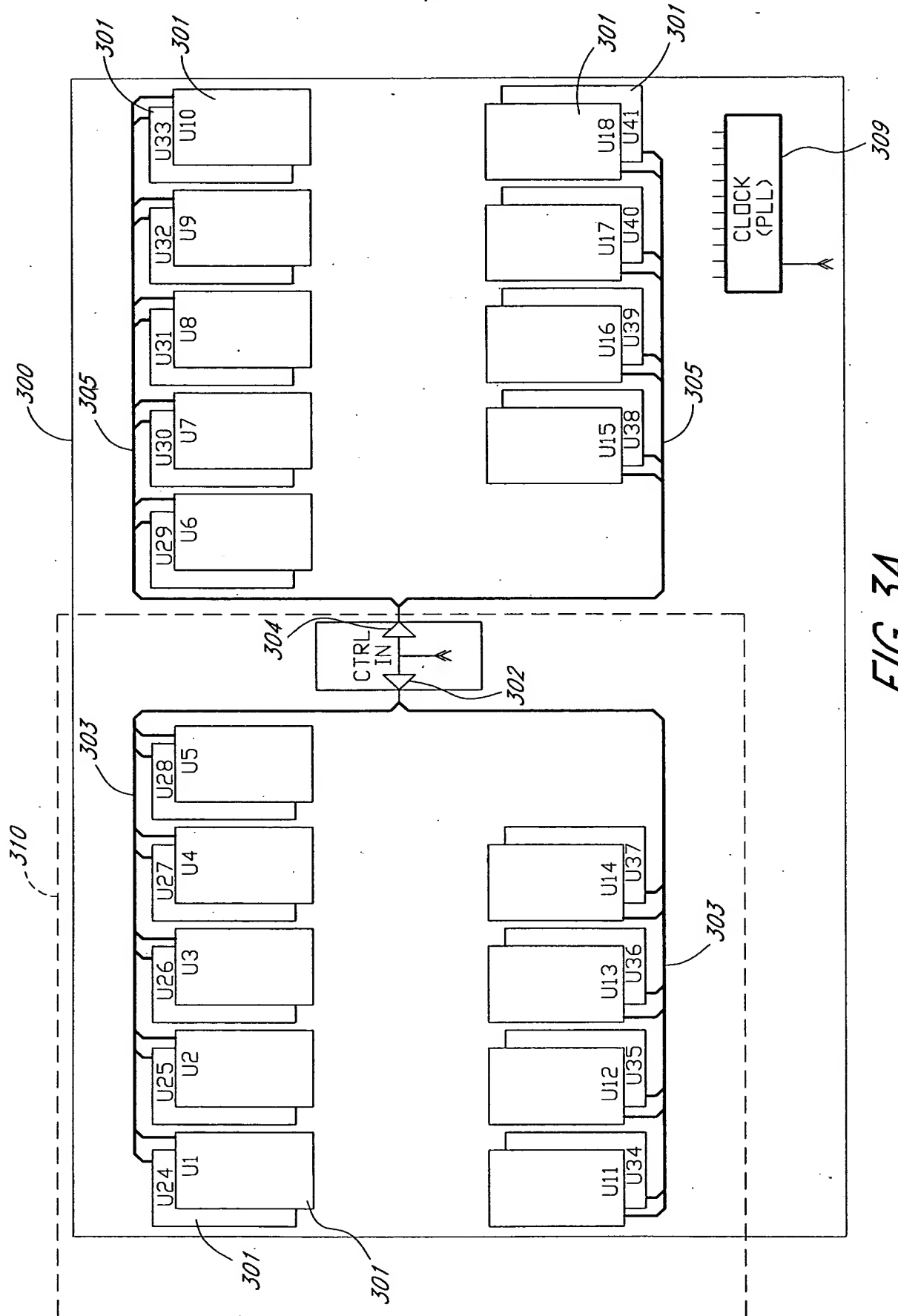
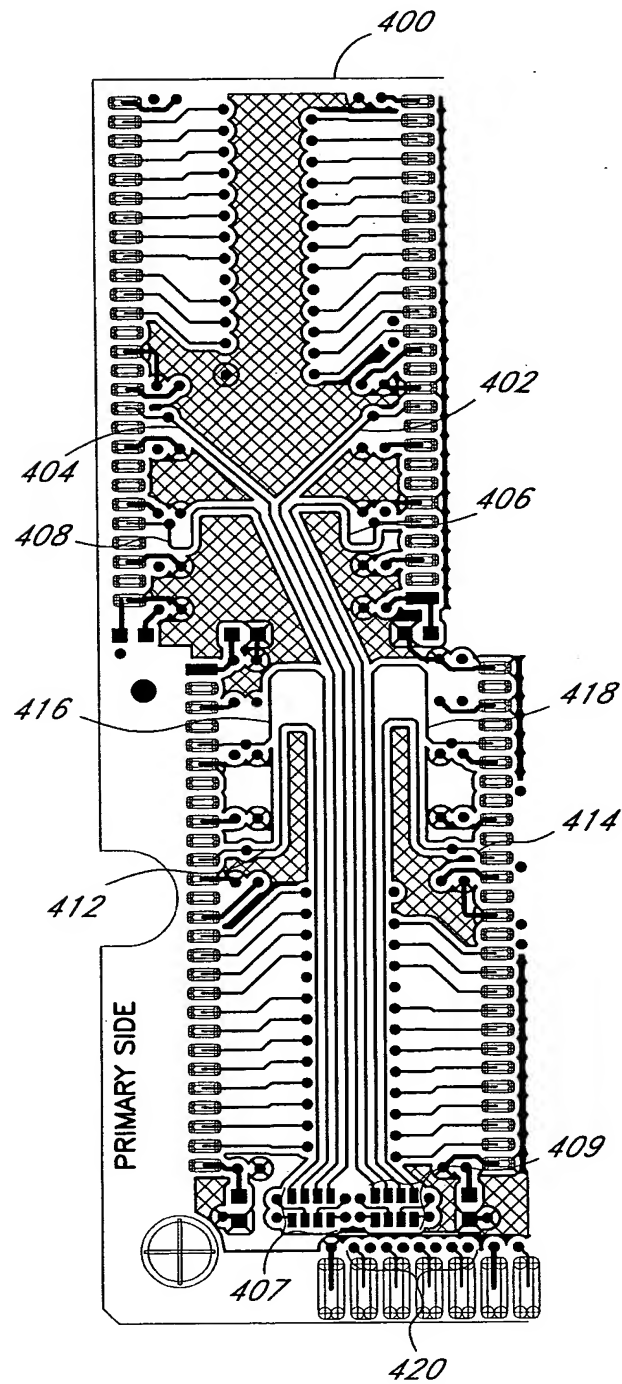


FIG. 3A



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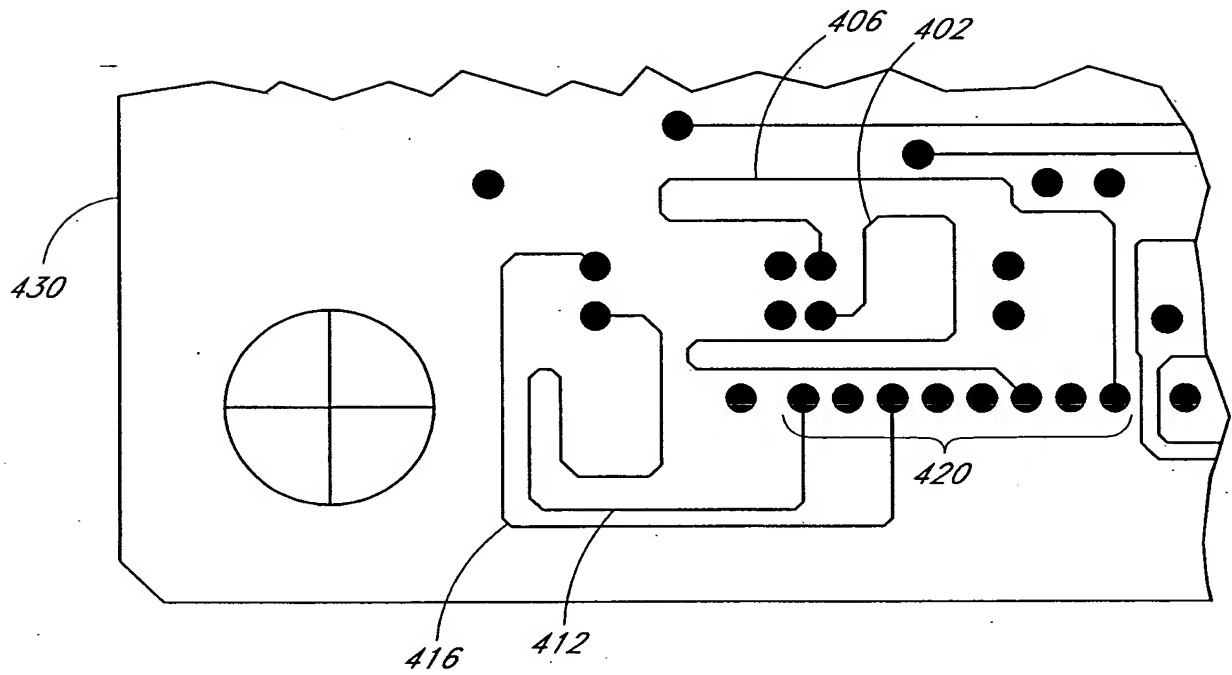


FIG. 4B

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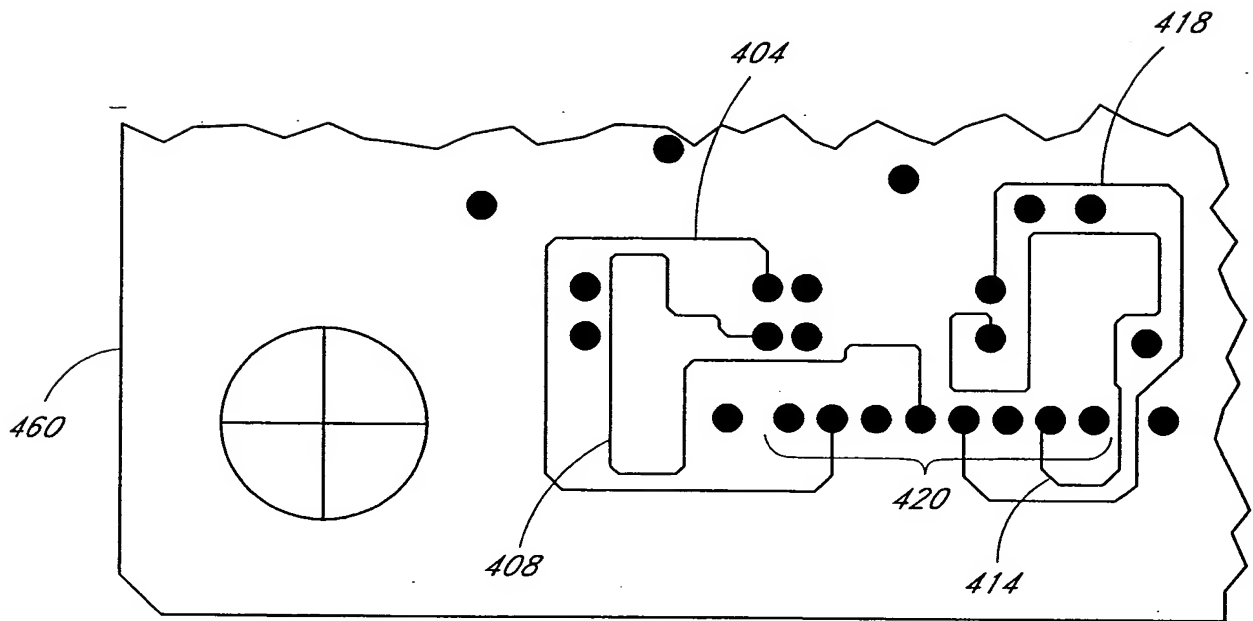


FIG. 4C